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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Brian G. Johnson	§	Art Unit: 2826
	§	
Serial No.: 10/633,874	§	Examiner: Thomas L. Dickey
	§	
Filed: August 4, 2003	§	Docket: ITO.0047US
	§	P16202
For: Lateral Phase Change	§	
Memory	§	Assignee: Intel Corporation
	§	

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL REPLY TO FINAL REJECTION

Sir:

In a supplemental response to the final rejection mailed April 10, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: October 6, 2006
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.

Cynthia L. Hayden
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